

Form PTO-1449 (Rev. 2-32)		U.S. Department of Commerce Patent & Trademark Office		Atty. Docket No. Q64375		Serial No.: Confirmation No.: Not Assigned	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				Applicant: Joannes Mathilda Josephus SEVENHANS, et al.			
				Filing Date: May 16, 2001		Group: Not Assigned	
U.S. PATENT DOCUMENTS							
Examiner Initial		Document Number	Date	Name	Class	Sub-Class	Filing Date (if appropriate)
AS		4,726,039	02/16/88	Piesinger			
FOREIGN PATENT DOCUMENTS							
		Document	Date	Country	Class	Sub-class	Translation Yes/No
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)							
AS		Jan Sevenhans et al, "Trends in silicon radio Large Scale Integration: Zero IF Receiver! Zero & Q Transmitter! Zero Discrete Passives!, TOPICS IN CIRCUITS FOR COMMUNICATIONS, IEEE COMMUNICATIONS MAGAZINE, January 2000, Vol. 38, No. 1, pp. 142-147					
J		T. Tsukahara et al, "A 2V 2GHZ-SI Bipolar Direct-Conversion Quadrature Modulator", IEEE INTERNATIONAL SOLID STATE CIRCUITS CONFERENCE, Vol. 37, 16 February 1994, pp. 40-41, 306 XP000507053					
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EXAMINER:		Mandate			DATE CONSIDERED: 8.4.04		
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